

PATENT ABSTRACTS OF JAPAN

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(21)Application number : 63-208257 (71)Applicant : NEW JAPAN RADIO CO LTD
 (22)Date of filing : 24.08.1988 (72)Inventor : ISHIDA RICHI

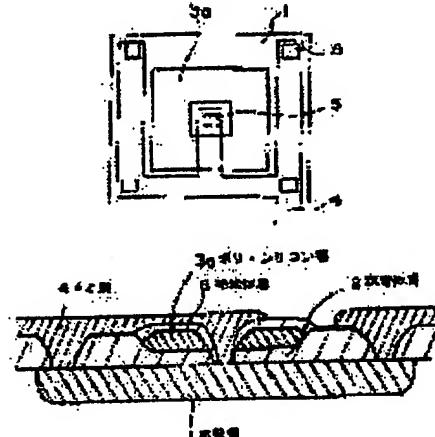
(54) INTEGRATED CIRCUIT

(57)Abstract:

PURPOSE: To reduce the internal resistance of a MOS type capacitor so as to make it easy to oscillate and to improve frequency stability by forming the other electrode to the diffusion layer region of one electrode of a MOS type capacitor in doughnut shape, and bringing the metallic film for taking out electrode into contact with the center, too, of the diffusion region.

CONSTITUTION: An electrode consisting of a polysilicon layer 3a is made in doughnut shape, and an Al film 4 is brought into contact with the center, too, of a diffusion layer 1 through the opening at the center provided in the polysilicon layer 3a. Hereby, the resistance amounts entering serially into the capacity decreases.

Accordingly, as compared with the equivalent resistance of a crystal oscillator, it can be restrained enough small, and it oscillates without fail, and the frequency stability becomes good.



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